

Session Title	Silicon Photonics 1	Session Code	4E2
Date and Time	July 4 (Wed.) / 10:30-12:00 (90')		
Place	Room E		
Session Chair	Daoxin Dai (Zhejiang Univ.)		

#### 4E2-1 (Invited)

10:30-11:00 (30')

##### Switching Devices and Systems based on Advanced Silicon Photonics

Kejiro Suzuki, Ryotaro Konoike, Satoshi Suda, Hiroyuki Matsuura, Kazuhiro Ikeda, Shu Namiki, and Hitoshi Kawashima  
AIST, Japan

#### 4E2-2 (Invited)

11:00-11:30 (30')

##### FPGA-Controlled Multi-Microring Network on Chip in Silicon Photonic Platform

N. Andriolli  
Scuola Superiore Sant'Anna, Italy

#### 4E2-3

11:30-11:45 (15')

##### Silicon Photonics Waveguide Chirped Gratings Enabled by Laser Interference Lithography

Chia-Wei Kao, Nai-Wen Cheng, You-Cheng Lu, Chia-Wei Huang, and Yung-Jr Hung  
Nat'l Sun Yat-sen Univ., Taiwan

#### 4E2-4

11:45-12:00 (15')

##### Proposal of Si Four-Wavelength Multiplexer using Higher-Order Mode for 100GbE

Junya Takano<sup>1</sup>, Takanori Sato<sup>1</sup>, Yusuke Sawada<sup>1</sup>, Takeshi Fujisawa<sup>1</sup>, Taiji Sakamoto<sup>2</sup>, Takashi Matsui<sup>2</sup>, Kyoza Tsujikawa<sup>2</sup>, Kazuhide Nakajima<sup>2</sup>, and Kunimasa Saitoh<sup>1</sup>  
<sup>1</sup>Hokkaido Univ., Japan, <sup>2</sup>NTT Corp., Japan