

Session Title	Silicon Photonics 1	Session Code	4E2
Date and Time	July 4 (Wed.) / 10:30-12:00 (90')		
Place	Room E		
Session Chair	Daoxin Dai (Zhejiang Univ.)		

4E2-1 (Invited)

10:30-11:00 (30')

Switching Devices and Systems based on Advanced Silicon Photonics

Keijiro Suzuki, Ryotaro Konoike, Satoshi Suda, Hiroyuki Matsuura, Kazuhiro Ikeda, Shu Namiki, and Hitoshi Kawashima
AIST, Japan

4E2-2 (Invited)

11:00-11:30 (30')

FPGA-Controlled Multi-Microring Network on Chip in Silicon Photonic Platform

N. Andriolli
Scuola Superiore Sant'Anna, Italy

4E2-3

11:30-11:45 (15')

Silicon Photonics Waveguide Chirped Gratings Enabled by Laser Interference Lithography

Chia-Wei Kao, Nai-Wen Cheng, You-Cheng Lu, Chia-Wei Huang, and Yung-Jr Hung
Nat'l Sun Yat-sen Univ., Taiwan

4E2-4

11:45-12:00 (15')

Proposal of Si Four-Wavelength Multiplexer using Higher-Order Mode for 100GbE

Junya Takano¹, Takanori Sato¹, Yusuke Sawada¹, Takeshi Fujisawa¹, Taiji Sakamoto², Takashi Matsui², Kyoza Tsujikawa², Kazuhide Nakajima², and Kunimasa Saitoh¹
¹Hokkaido Univ., Japan, ²NTT Corp., Japan