



Session Title	Photonic Integrated Circuits	Session Code	5D2
Date and Time	July 5 (Thu.) / 10:30-12:00 (90')		
Place	Room D		
Session Chair	TBA		

## 5D2-1 (Invited)

10:30-11:00 (30')

### Photonic Integrated WDM Cross-Connects for Telecom and Datacom Networks

Nicola Calabretta, Kristif Prifti, and Xuwei Xue  
Eindhoven Univ. of Tech., Netherlands

## 5D2-2 (Invited)

11:00-11:30 (30')

### Photonic Integrated Circuits Value Chain in Europe: from Components to Application

A. Gonzalez  
European Photonics Industry Consortium (EPIC), France

## 5D2-3

11:30-11:45 (15')

### Optical Phased Array based on Silicon Waveguides with Non-Uniform Widths

Jingye Chen<sup>1</sup>, Yanling Sun<sup>2</sup>, Shijie Wei<sup>2</sup>, Xiang'e Han<sup>2</sup>, and Yaocheng Shi<sup>1</sup>  
<sup>1</sup>Zhejiang Univ., China, <sup>2</sup>Xi'dian Univ., China

## 5D2-4

11:45-12:00 (15')

### Monolithically Integrated Optical NAND Gate using Light-Emitting Transistors

Hsuan-Han Chen, Chi-Wei Wang, and Chao-Hsin Wu  
Nat'l Taiwan Univ, Taiwan